

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17	processor AND (prevention OR protection) AND intrusion AND memory AND (enclosure OR housing) AND 713/193.ccls. AND (@pd<"20040126" or @ad<"20040126" or @prad<"20040126" or @rlad<"20040126")	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 08:45
L3	31	processor AND (prevention OR protection) AND intrusion AND memory AND (enclosure OR housing) AND 713/189,193, 194.ccls. AND (@pd<"20040126" or @ad<"20040126" or @prad<"20040126" or @rlad<"20040126")	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 08:49
L4	36	(processor microprocessor "CPU") AND (prevention OR protection) AND intrusion AND memory AND (enclosure OR housing) AND 713/189,193,194.ccls. AND (@pd<"20040126" or @ad<"20040126" or @prad<"20040126" or @rlad<"20040126")	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 08:51
L5	37	(processor microprocessor "CPU" control unit) AND (prevention OR protection) AND intrusion AND memory AND (enclosure OR housing) AND 713/189, 193,194.ccls. AND (@pd<"20040126" or @ad<"20040126" or @prad<"20040126" or @rlad<"20040126")	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 08:51
S1	18	(prevention OR protection) AND intrusion AND memory AND (enclosure OR housing) AND 713/193.ccls. AND (@pd<"20040126" or @ad<"20040126" or @prad<"20040126" or @rlad<"20040126")	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/09/07 09:40